



SHRI VILEPARLE KELAVANI MANDAL'S  
DWARKADAS J. SANGHVI COLLEGE OF ENGINEERING  
Approved by AICTE and Affiliated to the University of Mumbai



# *ACADEMIC BULLETIN*

*July 2017- December 2017*

**Department of Electronics &  
Telecommunication Engineering**

Prepared By:

Ms. Archana Chaudhari

(Assistant Professor, EXTC, DJSCE)

Dr. Amit A. Deshmukh

(Professor & Head EXTC, DJSCE)



## ACADEMIC BULLETIN

**Period: 1<sup>st</sup> July' 2017 – 31<sup>st</sup> December' 2017**

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## 1. ABOUT DEPARTMENT

### 1.1 Vision

To be a world class Institution for education, training and research in engineering, inculcating values and skills for sustainable development of the society.

### 1.2 Mission

- To provide competent faculty and an interactive learning environment along with world class infrastructure for nurturing professionalism & entrepreneurship in Engineers.
- To foster technical competence, research aptitude and environmental awareness amongst aspiring technocrats to develop sustainable engineering solutions.
- To provide a forum for active interaction between academia & industry, leading to continuous improvement in engineering education.

### 1.3 Vision of the Department

To develop technically competent and socially responsible Electronics and Telecommunication engineers capable of fulfilling expectations at indigenous and global levels.

### 1.4 Mission of the Department

- To provide a conducive educational environment for students by providing good infrastructural facilities, knowledge base and excellent faculty support.
- To provide a strong foundation of core knowledge and exposure to research culture.
- To motivate learners to acquire adequate professional and soft skills, to develop personality traits and eventually transform them as life-long learners.
- To strive and achieve practical exposure by maintaining good rapport with industry and professional network.

### 1.5 Program Educational Objectives (PEOs)

- **PEO1:** To prepare learners for graduate studies by providing strong foundation of basic sciences, computer programming and thus, develop analytical aptitude, and problem solving abilities.
- **PEO2:** To develop a fundamental understanding of electronic & integrated circuits,



communication systems and allied disciplines.

- **PEO3:** To develop core competency and expertise in the diverse areas of communication covering Signal processing, Electromagnetic Engineering, Embedded Systems, Computer Communication and Advanced Wireless Networks domains.
- **PEO4:** To inculcate competencies and aptitude in extending acquired technical knowledge to solve real life issues with high professional and ethical standards.
- **PEO5:** To develop proficiency in soft skills and deliver adequate personality traits to enable the pass outs to pursue higher education, to find competitive employment opportunities and/or pursue entrepreneurial ventures.

## 1.6 Department Information

- Started in the year 1999 with the intake of 30 and which was increased to 60 in the subsequent year.
- The intake was increased to 120 in the Academic Year 2010 – 11.
- In the Academic Year 2011 – 12, Department has started M.E. Program in Electronics & telecommunication with an intake of 18 students.
- Department got NBA accreditation for two years from January 2013.
- The Department started with Ph.D. program in Academic Year 2015 – 16 with an intake of 10 students.
- The department is having highly qualified, experienced and dedicated faculties and supporting staff.
- Well-equipped labs and fully air conditioned classrooms with projectors



## 2. ADMINISTRATION

### IETE COMMITTEE

Dr. Amit Deshmukh

Dr. M. H. Patwardhan

Prof. Anuja Odhekar

### PROJECT COORDINATOR

Dr. Amit Deshmukh

Prof. Ameya Kadam

Prof. Ranjushree Pal

### TECHNOFOCUS COMMITTEE

Dr. Amit Deshmukh

Prof. Shivani Bhattacharjee

Prof. Revati A. S.

### DEPARTMENTAL LIBRARY

Dr. Amit Deshmukh

Prof. Archana Chaudhari

### ALUMNI COMMITTEE

Prof Anuja Odehekar

Prof. Mrunalini Ingle

### EXAM COMMITTEE

Prof. Venkata A. P. Chavali

Prof. Arati Ambekar

Prof. Yukti Bandi

### ADMISSION COMMITTEE

Dr. M. H. Patwardhan

Prof. V. V. Kelkar

Prof. Ameya Kadam

### NBA CORE COMMITTEE

Dr. Amit Deshmukh

Dr. M. H. Patwardhan

Prof. Ameya Kadam

Prof. Poonam Kadam

Prof S. B. Deshmukh

Prof. Venkata A. P. Chavali

### ANTIRAGGING CELL

Prof. T. D. Biradar

Prof. V. V. Kelkar

### ANTIRAGGING SQUAD

Dr. Amit Deshmukh

Dr. M. H. Patwardhan

### STUDENT COUNCIL

Dr. M. H. Patwardhan

Dr S. H. Karamchandani

### SPORTS COMMITTEE

Prof. Ameya Kadam

### TECHNICAL CHAIR PERSON

Prof. T. D. Biradar

### TIME-TABLE COMMITTEE

Prof. Poonam Kadam

### NPTEL COORDINATOR

Prof. Vishakha Kelkar

### PLACEMENT COORDINATOR

Prof. Arati Ambekar

### WOMEN DEVELOPMENT CELL

Prof. V. V. Kelkar



### 3. IETE- SF

The Electronics and Telecommunication Department of Dwarkadas. J. Sanghvi College of Engineering presents Institution of Electronics and Telecommunication Engineers- Student Forum (IETE-SF). The student chapter with a working force committee of 22, consisting of **second year** and **third year students**, hosted a few of the most quintessential and technically challenging events. A membership drive was conducted at the start of the year with an overwhelming response.

#### IETE Organizing Committee Structure

**Faculty Incharge :-** Prof. Mrinal Patwardhan, Prof. Anuja Odhekar

<b>Chairman</b>	Heetika Gada
<b>Vice-Chairman</b>	Aditi Agrawal
<b>Secretary</b>	Pooja Jha
<b>Treasurer</b>	Asmita Dabholkar
<b>Jt.Secretary</b>	Megh Doshi Parth Thakar

<b>Head Of Departments :</b>	
<b>Editorial Head</b>	Mrudang Langalia, Preethi Abraham
<b>Publicity</b>	Aman Bhargava
<b>Marketing</b>	Jatan Mehta
<b>Technical</b>	Kinjal Sawala
<b>Infotech</b>	Jugal Makwana
<b>Creatives</b>	Swarali Desai
<b>Events</b>	Vedant Gokani
<b>Logistics</b>	Aditya Desai



### 3.1 Value Added Program

#### Book Bank

Book Bank is an initiative made by IETE that makes **reference books** available to students at **10% of the original cost**. It improved the core competency and to strengthen the teaching ability. These books are referred by the faculty members and hence it makes the studying process efficient and helps to increase the student's technological knowledge about the subject. It also helps to build a foundation of the concepts that could enhance the practical skills required in the future. It gets updated every year and has several books to offer currently.

## 4. DEPARTMENT ACTIVITIES UNDER IETE-SF

### 4.1 ARDUINO workshop

IETE SF's 'FORTNIGHT', had its Second Event as the Arduino Workshop. It was held over a span of 2 days, 18<sup>th</sup> and 21<sup>st</sup> of September and was conducted by a Third Year student, Megh Doshi, who is the Joint Secretary of IETE and is also a part of a Mechanical Team. In all 83 students(21 team) were participated in the event.







## 4.2 ICWiCOM-2017

ICWiCOM-2017 (International Conference on wireless communication -2017) is organized by Electronics & Telecommunication Engineering. The ICWiCOM-2017 is scheduled **on 19<sup>th</sup> – 20<sup>th</sup> January, 2018**. The conference aims at various research work in different areas of communication networks, signal processing and RF& Antennas. The conference will have plenary and technical sessions. All accepted and presented papers based on original findings will be send for publication in Springer international journal.

## 4.3 Ethical Hacking Workshop

IETE-SF conducted a workshop on Ethical Hacking, for First year students, on the 1st and 2nd of October between 9:30am to 4:30pm, as part of Fortnight. The workshop was conducted by Mr. Sachin Dedhia, Jerry Thomas and their team.

Ethical hacking is the act of locating weaknesses and vulnerabilities of computer and information systems by duplicating the intent and actions of hackers. Ethical hacking is a penetration or intrusion testing technique. It is performed by a company or an individual to help identify potential threats on a computer or network. Participants were introduced to the importance of ethical hacking tools, understanding the process, and implementing it. The ethical hacking system includes some of the services like: Application Testing, war dialing, network testing, wireless security and system hardening.





#### **4.4 How to Make a Resume seminar**

IETE-SF conducted a seminar on 'How to make a Resume' as the fifth event of 'Fortnight' on 28th of September. A Resume is a summary of a person's background, experiences, training and skills. Most professional positions require the applicant to submit a Resume, hence knowing how to make a Resume is of utmost importance. The seminar was conducted by Prof. Shirley Mathew in the seminar hall from 3.30 pm to 4.30 pm.



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## 4.5 How to Write a Technical Paper

IETE-SF conducted a seminar on 'How to write a Technical Paper' as the third event of 'Fortnight' on 27th of September. A technical paper is a document that describes the process, progress, or results of technical or scientific research. The workshop was conducted from 4 pm to 6 pm by Prof. Mrinal Patwardhan, in the seminar hall with more than 60 students attended the event.



## 4.6 MATLAB Workshop

MATLAB, which stands for Matrix Laboratory, is a high-performance language created by Mathworks Inc. for technical computing. It integrates computation, visualization, and programming in an easy-to-use environment where problems and solutions are expressed in familiar mathematical notation. The workshop was conducted by Prof. V Venkataramanan, in the R.F. Lab, EXTC Dept, from 9.30 am to 4.30 pm. The workshop was divided into two halves. The first half of the session mainly covered basics of MATLAB. It included everything from creating a file in MATLAB to working with different operators, interactive commands, arrays and plotting graphs of different functions.



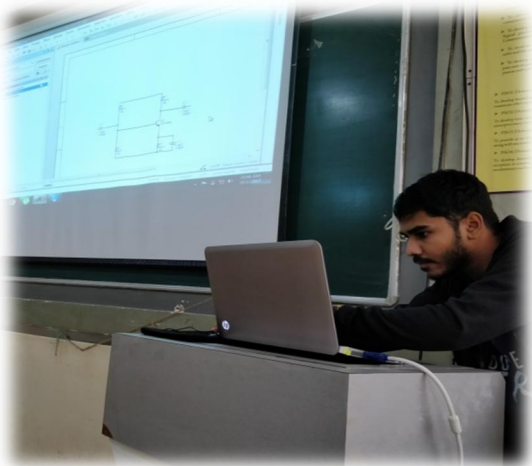


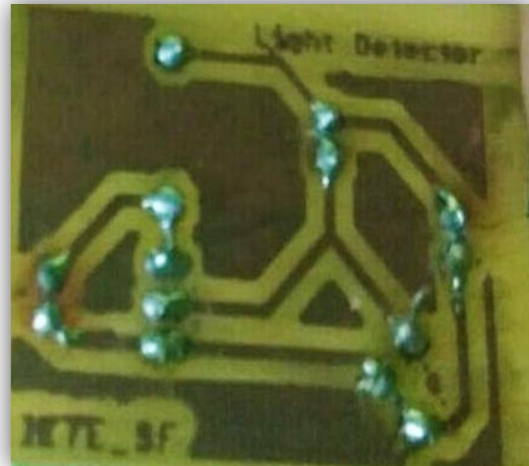


## 4.7 PCB Making Workshop

A two day workshop on 'PCB making' was conducted on 1st and 2nd October, 2017, as part of IETE-SF's 'FORTNIGHT'. The first day of the workshop was conducted by Kinjal Savla who is the Technical Head of IETE-SF. She started off by giving a brief introduction on what a PCB is. The importance of PCB designing and the benefits of it were also explained. The 'Altium designer' software was used by the workshop attendees for the purpose of designing the PCB. Their first task was to design a 'CE-Amplifier' with the aid of the Altium designer software. The software design tools required to design the amplifier was explained thoroughly by Kinjal. Concepts like footprint, manual routing, auto routing, cross connections, netlist and tracking process were made clear by her. After the completion of the first task, the workshop attendees were taught how to design a timer circuit using IC 555. Finally the participants were taught how to create their own libraries using the Altium Designer software.

The second day of the workshop was jointly conducted by Pooja Jha, who is the secretary of IETE-SF and Shivam Pandey from the technical co-committee of IETE-SF. The participants were assigned the task to design and implement a light detector circuit. PCB making skills like printing, ironing, cleaning, etching and soldering were covered in the workshop.





#### 4.8 Power Electronics seminar

IETE-SF conducted a lecture on Power Electronics as the third event of 'Fortnight' on 25th of September. Power Electronics is the study of switching electronic circuits in order to control the flow of electrical energy. It is the technology behind switching power supplies, power converters, power inverters and many other common applications. The workshop was conducted from 3 pm to 5.30 pm by Prof. Niharika Maliwar and Prof. Ameya Kadam.







## 5. ACHIEVEMENTS

### 5.1 Faculty Publications- Conferences / Journals

Sr.No.	Name of Faculty	Publications
1	Dr. Amit Deshmukh	<ol style="list-style-type: none"><li>1. Amit A. Deshmukh, Pritesh Kamble, Akshay Doshi, Disha Issrani and K. P. Ray, "Proximity Fed Broadband <math>120^0</math> Sectoral Microstrip Antenna" , Proceedings of ICACC – 2017, 22<sup>nd</sup> – 24<sup>th</sup> August 2017, Kochi, India (Volume 115, 2017, pp. 108–114, <a href="https://doi.org/10.1016/j.procs.2017.09.083">https://doi.org/10.1016/j.procs.2017.09.083</a>, <a href="http://www.sciencedirect.com/science/article/pii/S1877050917318902">http://www.sciencedirect.com/science/article/pii/S1877050917318902</a>)</li><li>2. Amit A. Deshmukh, Akshay Doshi, Pritesh Kamble, Disha Issrani and K. P. Ray, "Modified Triangular Shape Microstrip Antenna For Circular Polarization", Proceedings of ICACC – 2017, 22<sup>nd</sup> – 24<sup>th</sup> August 2017, Kochi, India (Volume 115, 2017, pp. 101–107, <a href="https://doi.org/10.1016/j.procs.2017.09.082">https://doi.org/10.1016/j.procs.2017.09.082</a>, <a href="http://www.sciencedirect.com/science/article/pii/S1877050917318896">http://www.sciencedirect.com/science/article/pii/S1877050917318896</a>)</li><li>3. Amit A. Deshmukh, Poonam Kadam, Darshan Gala and Akshay Doshi, "Wide Band Designs of Rectangular Microstrip Antenna Using Modified Ground Plane", Accepted for publication in ICAC3 – 2017, 1<sup>st</sup> &amp; 2<sup>nd</sup> December 2017, Mumbai, India (IEEE digital library)</li><li>4. Amit A. Deshmukh, Pritish Kamble, Venkata A. P. C., Akshay Doshi and K. P. Ray, "Gap-Coupled Variations of <math>120^0</math> Sectoral Shape Microstrip Antennas For Wideband Response", Accepted for publication in AEMC 2017, 19<sup>th</sup> - 22<sup>nd</sup> December 2017, Aurangabad, India (IEEE digital library)</li></ol>



		<p>5. Amit A. Deshmukh, Aarti G. Ambekar, Venkata A. P. C., Akshay Doshi and K. P. Ray, "Modified U-slot Cut Rectangular Patch Antenna For Wideband Response", Accepted for publication in AEMC 2017, 19<sup>th</sup> - 22<sup>nd</sup> December 2017, Aurangabad, India (IEEE digital library)</p> <p>6. Amit A. Deshmukh, Shefali Pawar, Aarti G. Ambekar, Pritish Kamble and K. P. Ray, "Compact Y-shape Antenna For Dual Polarized Wideband Response" Accepted for publication in AEMC 2017, 19<sup>th</sup> - 22<sup>nd</sup> December 2017, Aurangabad, India (IEEE digital library)</p>
2	Prof. Vishakha Kelkar	<p>1. Vishakha Kelkar, Kushal Tuckley, Hitesh Nemade, "Novel Variants of a Histogram Shift-Based Reversible Watermarking Technique for Medical Images to Improve Hiding Capacity" Hindawi , Journal of Healthcare Engineering ,Volume 2017, Article ID 3538979, 7 pages <a href="https://doi.org/10.1155/2017/3538979">https://doi.org/10.1155/2017/3538979</a>.</p> <p>2. Vishakha Kelkar, Jinal Mehta and Kushal Tuckley, "A Novel Robust Reversible Watermarking Technique based on Prediction Error Expansion for Medical Images" CVIP-WM 2017, IIT Rurkee , 9-12 Sept.2017(Will be Published in IEEE Digital Library)</p> <p>3. Vishakha Kelkar, Jinal Mehta and Kushal Tuckley, "A Robust Reversible Watermarking Technique based on Prediction Error Expansion using ECC " International Conference on Advances in Computing Communications and Informatics (ICACCI'17) Manipal University, Manipal, Karnataka, India September 13-16, 2017(Will be Published in IEEE Digital Library)</p>



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3	Dr. Sunil Karamchandani	<ol style="list-style-type: none"><li>1. Aniruddha Garge, Dr. Sunil Karamchandani, and Dr. Zia Saquib,” Performance Evaluation Of Variable And Modified Gabor Kernels For Filtering Of Fingerprint Images”, 8th International Conference on Computing Communication and Networking Technologies 2017, IIT Delhi, July 3-5,2017 ICCCNT2017.</li><li>2. Aniruddha Garge, Dr. Sunil Karamchandani, and Dr. Zia Saquib,” Performance Comparison Of Orientation Sensitive Filters For Low Quality Fingerprints”, ISPCC2017, 4th International Conference on Signal Processing, Computing and Control (ISPCC 2017) will be organized by Jaypee University of Information Technology, Waknaghat, India.</li><li>3. S. Karamchandani, A. Mehta, Yash Shah, Performance Evaluation of Machine Learning and Deep Learning Techniques for Sentiment Analysis, 4<sup>TH</sup> INTERNATIONAL CONFERENCE ON INFORMATION SYSTEM DESIGN AND INTELLIGENT APPLICATIONS, INDIA 2017, 15-17<sup>th</sup> June, Da Nang, Vietnam.</li><li>4. S. Karamchandani, Score Formulation and Parametric Synthesis of Musical track as a platform for Big Data in Hit Prediction, ICACCP 2017, 8-10 September, smit, Sikkim, India.</li></ol>
4	Prof. Ranjushree Pal	<ol style="list-style-type: none"><li>1. Ranjushree Pal, “Comparison of the design of FIR and IIR filters for a given specification and removal of phase distortion from IIR filters” International conference on Computing, Communication and Control, 1-2 December, 2017, IEEE conference, organized by Fr. Conceicao Rodrigues College of Engineering, Mumbai.</li></ol>



## 5.2 Interaction of faculties with outside world

Sr. No.	Name of Faculty	Description
1	Dr. Amit Deshmukh	1. On the examination panel for PhD Annual progress seminar at VJTI mumbai (August 2017) & UMIT, SNDT, Mumbai (September 2017).
2	Prof. Ameya Kadam	1. Conducted workshop on "Impedance Matching using Smith chart" at Shree L.R.Tiwari college of engineering on 4th August 2017 for T.E. and B.E students
3	Dr. Sunil H. Karamchandani	1. Associate Faculty, Pedagogy for online learning and Blended Teaching process, organised by NMEICT, IIT Bombay under the eoutreach TTK programme.



## 6. Result Analysis

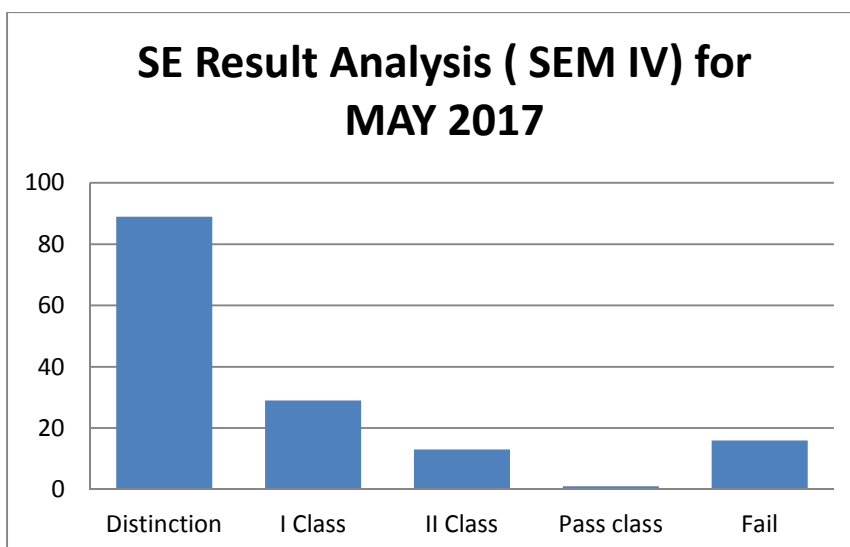
**Class:- SE**

No. of students: 148

	Distinction	I Class	II Class	Pass class	Fail
SE Result Analysis(SEM IV) MAY 2017	89	29	13	1	16

### Subject wise Result Analysis

Subject	Appeared	Pass
Applied Mathematics IV	148	144
Analog Electronics- II	148	142
Microprocessors and Peripherals	148	138
Wave Theory and Prpagation	148	145
Signals and Systems	148	145
Control Systems	148	145





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Name	Rank	GPA
Baleri Panchami	First	10
Roge Swapnil	First	10
Dixit Akshaya	First	10
Suhrid Subramaniam	Second	9.86
Bandeali Lizna	Second	9.86
Shah Dharati	Second	9.86
Desai Rumi	Third	9.81
Shah Nigam	Third	9.81

### Class: TE

Total No. of students = 144

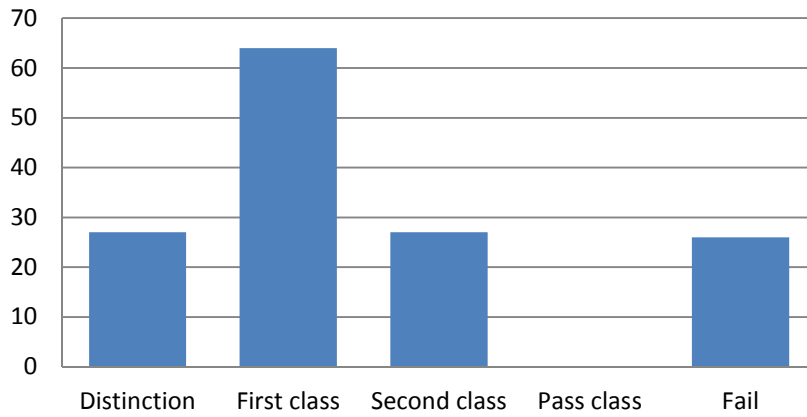
	Distinction	First class	Second class	Pass class	Fail
TE Result Analysis(SEM VI)- MAY 2017	27	64	27	0	26

### Subjectwise Result Analysis

Subject	Appeared	pass
Digital Communication	144	139
Discrete Time Signal Processing	144	122
Computer Communication and Networks	144	142
Television Engineering	144	144
Operating Systems	144	144
VLSI Design	144	128



### TE Result Analysis ( SEM VI) for May 2017



Name	Rank	GPA
Gandhi Prachi	First	9.86
Parmar Yash	Second	9.29
Zala Harshita	Second	9.29
Sangani Drashti	Third	9.14

#### Class: BE

Total No. of students = 152

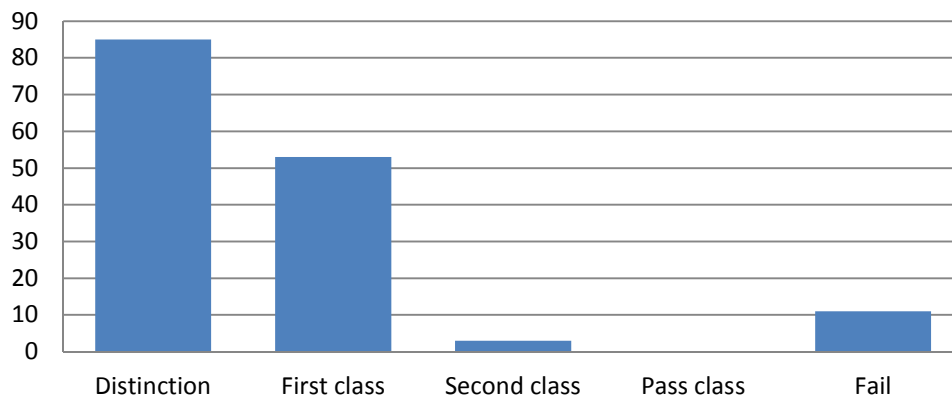
	Distinction	First class	Second class	Pass class	Fail
BE Result Analysis(SEM VIII)- MAY 2017	85	53	3	0	11



Subjectwise Result Analysis

Subject	Appeared	pass	fail
Wireless Networks	152	150	2
Satellite Communication and Networks	152	148	4
Internet and Voice Communication	152	150	2
Speech Processing	152	150	2
Subject	Appeared	pass	fail
Wireless Networks	152	150	2

**BE Result Analysis ( SEM VIII) for MAY 2017**



Name	Rank	GPA
Korani Khusboo	First	9.53
Koyani Amber	Second	9.32
Patel Stuti	Third	9.3





## 7. Placement Data

Total no. of Students placed = 90

Sr. No.	Company Name	No. of Students Placed	Salary Per Annum(LPA)
1.	ZS Associates	3	6.75
2.	Indus Valley Partners	1	7.48
3.	Halftick	1	6
4.	ENY	3	4.5
5.	Quantiphi	2	5.5
6.	Tresvista	3	5.08
7.	TCS	24	3.3
8.	Infosys	18	3.25
9.	NSE TECH	1	5.4
10	ATOS	4	3.1
11	LNTI	12	4.1
12.	NSE IT	3	3
13.	Aufklaren	1	2.1
14.	Deloitte	3	4.1
15.	Media.net	1	3.1
16.	Musigma	4	3.5+5signing Bonus
17.	Oracle(OFSS)	2	6
18.	Pharmeasy	1	10
19.	Selec	3	3 to 4
<b>Minimum CTC in LPA: 2.1 LPA</b>		<b>Maximum CTC in LPA : 10.0 LPA</b>	



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*January 2018- June 2018*

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(Assistant Professor, EXTC, DJSCE)

Dr. Amit A. Deshmukh

(Professor & Head EXTC, DJSCE)



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  - 1.6 Program Educational Objectives (PEOs)
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3. IETE-SF
  - 3.1 Value Added Program (Book Bank, Component Bank)
4. Department Activities under IETE-SF
  - 4.1 Lecture on Smith Chart
  - 4.2 Speech and audio processing technical talk
  - 4.3 Artificial Intelligence and Computer Vision workshop
  - 4.4 Tech- Talk on AI & Machine Learning
  - 4.5 Industrial Visit to GMRT
  - 4.6 ICWiCOM 2017
  - 4.7 DJS ARYA (CANSAT)
  - 4.8 D J Spark
  - 4.9 D J Strike
  - 4.10 D J IGNITE
  - 4.11 Pre- Placement activity
5. Achievements
  - 5.1 Faculty Publications- International Conference/ Journal
  - 5.2 Interaction of faculties with outside world



### 5.3 Students Achievements

6. Result Analysis
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## 1. ABOUT DEPARTMENT

### 1.1 Vision

To be a world class Institution for education, training and research in engineering, inculcating values and skills for sustainable development of the society.

### 1.2 Mission

- To provide competent faculty and an interactive learning environment along with world class infrastructure for nurturing professionalism & entrepreneurship in Engineers.
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- To motivate learners to acquire adequate professional and soft skills, to develop personality traits and eventually transform them as life-long learners.
- To strive and achieve practical exposure by maintaining good rapport with industry and professional network.



### 1.5 Program Specific Outcomes (PSOs)

- To develop knowledge in the domain of signal analysis and processing and provide a foundation to numerous other courses that deals with signal processing applications.
- To develop basic and applied knowledge of the architecture and assembly language programming for microprocessor/microcontroller based systems, along with the peripheral interfacing.
- To provide an in-depth understanding of electromagnetics, transmission lines and antenna concepts along with microwave devices used for RF and microwave applications.
- To develop knowledge of the fundamental techniques related to generation, transmission and reception in communication systems for a wide range of wired and wireless applications along with revolutionary technology developments.

### 1.6 Program Educational Objectives (PEOs)

- **PEO1:** To prepare learners for graduate studies by providing strong foundation of basic sciences, computer programming and thus, develop analytical aptitude, and problem solving abilities.
- **PEO2:** To develop a fundamental understanding of electronic & integrated circuits, communication systems and allied disciplines.
- **PEO3:** To develop core competency and expertise in the diverse areas of communication covering Signal processing, Electromagnetic Engineering, Embedded Systems, Computer Communication and Advanced Wireless Networks domains.
- **PEO4:** To inculcate competencies and aptitude in extending acquired technical knowledge to solve real life issues with high professional and ethical standards.
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- The intake was increased to 120 in the Academic Year 2010 – 11.
- In the Academic Year 2011 – 12, Department has started M.E. Program in Electronics & telecommunication with an intake of 18 students.
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Prof. Archana Chaudhari

### ALUMNI COMMITTEE

Prof Shivani Bhattacharjee

Prof. Poonam Kadam

### ADMISSION COMMITTEE

Prof. V. V. Kelkar

Prof. Ameya Kadam

### NBA CORE COMMITTEE

Dr. Amit Deshmukh

Prof. V. V. Kelkar (PC/NC) Prof. Ameya Kadam

Prof. Poonam Kadam

Prof S. B. Deshmukh Prof. Venkata A. P. Chavali

### ANTIRAGGING CELL

Prof. T. D. Biradar

Prof. V. V. Kelkar

### EXAM COMMITTEE

Prof. Venkata A. P. Chavali

### NSS Program Coordinator

Prof. Rahul Taware

### ANTIRAGGING SQUAD

Dr. Amit Deshmukh

### DJSCE NEN

Dr S. H. Karamchandani

### SPORTS COMMITTEE

Prof. Ameya Kadam

### TECHNICAL CHAIR PERSON

Prof. T. D. Biradar

### TIME-TABLE COMMITTEE

Prof. Poonam Kadam

### NPTEL COORDINATOR

Prof. V. V. Kelkar

### PLACEMENT COORDINATOR

Prof. Aarti Ambekar

### WOMEN DEVELOPMENT CELL

Prof. V. V. Kelkar





### 3. IETE- SF

The Electronics and Telecommunication Department of Dwarkadas. J. Sanghvi College of Engineering presents Institution of Electronics and Telecommunication Engineers- Student Forum (IETE-SF). The student chapter with a working force committee of 22, consisting of **second year** and **third year students**, hosted a few of the most quintessential and technically challenging events. A membership drive was conducted at the start of the year with an overwhelming response. ([www.djsceietesf.com](http://www.djsceietesf.com))

#### IETE Organizing Committee Structure

Faculty Incharge :- Prof. Anuja Odhekar

<b>Chairman</b>	Heetika Gada
<b>Vice-Chairman</b>	Aditi Agrawal
<b>Secretary</b>	Pooja Jha
<b>Treasurer</b>	Asmita Dabholkar
<b>Jt.Secretary</b>	Megh Doshi Parth Thakar

<b>Head Of Departments :</b>	
<b>Editorial Head</b>	Mrudang Langalia, Preethi Abraham
<b>Publicity</b>	Aman Bhargava
<b>Marketing</b>	Jatan Mehta
<b>Technical</b>	Kinjal Sawala
<b>Infotech</b>	Jugal Makwana
<b>Creatives</b>	Swarali Desai
<b>Events</b>	Vedant Gokani
<b>Logistics</b>	Aditya Desai



### 3.1 Value Added Program

#### Book Bank

Book Bank is an initiative made by IETE that makes **reference books** available to students at **10% of the original cost**. It improved the core competency and to strengthen the teaching ability. These books are referred by the faculty members and hence it makes the studying process efficient and helps to increase the student's technological knowledge about the subject. It also helps to build a foundation of the concepts that could enhance the practical skills required in the future. It gets updated every year and has several books to offer currently.

#### Component Bank

DJSCE IETE-SF proudly introduces the **Component Bank Facility**, through which students can benefit by borrowing components they require at a lower price and return them once their job is done. The worry of buying expensive components and then thinking about what to do with them once the project is finished, is eliminated.

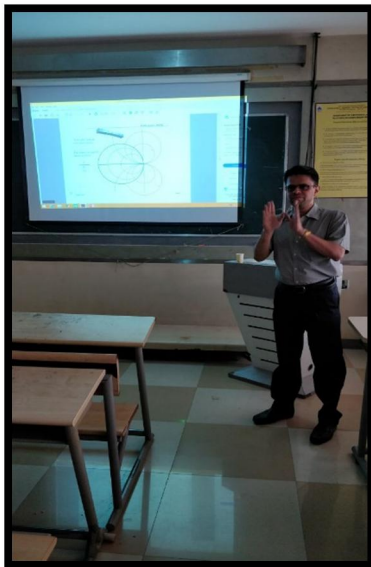


## 4. DEPARTMENT ACTIVITIES UNDER IETE-SF

### 4.1 Lecture on Smith Chart

IETE-SF organised a lecture on Smith Charts. It was two hour long lecture, conducted on 17th of February, at 4.00 pm onwards. The lecture was delivered by Dr Amit A. Deshmukh, Professor and Head of EXTC department, with students as well as teachers in attendance.

Smith Chart is a powerful tool for microwave analysis and antenna design. Because of its simplicity, it is also one of the most widely used tools, which makes it a very important topic. Since it is not included in the curriculum, IETE-SF took the initiative to educate students in this respect. The event was well organised and IETE-SF's team received amazing feedback for this initiative and their hard work.





## 4.2 Speech and Audio Processing technical talk

IETE-SF organized a technical talk on speech and audio processing which was held on 20<sup>th</sup> February, 2018. Dr. Milind Shah (HOD, EXTC, Fr. Agnel College of Engineering, Vashi) was the speaker for the talk.

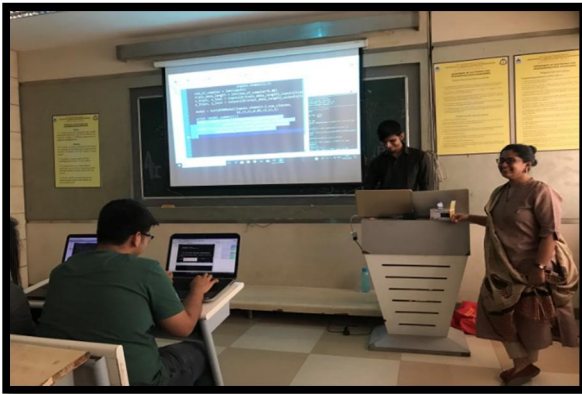
This technical talk was truly engaging and knowledge worthy. Dr. Shah's session encouraged the third year students among the audience to take up speech processing as their elective subject for the next semester whereas the final year students among the audience who have already opted for this subject as an elective gained additional knowledge apart from their regular curriculum awareness.



### 4.3 Artificial Intelligence and Computer Vision workshop

IETE-SF conducted a workshop on Artificial Intelligence and Computer vision. It was a 35-hour long workshop, conducted over a span of six days: 21st, 26th, 27th, 28th of January and 3rd and 4th of February. The workshop was conducted by Department Alumni, Roma Jain and Sameer Kadam (batch of 2017) with over 50 participants in attendance.

The workshop ended with a general discussion on the Artificial Intelligence domain and the correlation between the concepts of Computer vision and deep learning. On the last day of the workshop, every participant was awarded a participation certificate. The session was highly interactive, with a lot of practical learning, which made it all the more interesting and fun.





#### 4.4 Tech-Talk on AI & Machine Learning

IETE-SF conducted a 'Tech Talk' By Mr. Rajeev Raval on 6th of March, 2018. He is an Electronics Engineer from Ramrao Adik College of Engineering with 22 years of experience with multinational IT companies like Ashtech Infotech, Tata Elxsi, Reuters and NCR corp. He is currently working as a Sr. Deal Architect & Head in Pre-Sales and Security Solutions at BT Global Services.

Mr. Rajeev began the talk with a simple yet enlightening question 'What is an engineer?' By engaging students, he explained the basic concept- To creatively solve problems without compromising on the quality of the service. Tech trends like Artificial Intelligence, Internet of Things, Block Chain, Augmented and Virtual Reality, Cloud, Big Data, Security terminologies were the kind of topics he spoke at great length.

He gave real life examples to explain how the biggest Tech companies like Google, Amazon and Facebook were created, the journey of their growth, how they use cloud and the revolutionary way of linearly storing data to explain the implementation of the tech trends. The students were explained how minimum hardware was used to optimize cloud storage by data compression. The concept of bare metal coding was introduced to them. Virtualization Technology is used widely in Networks, servers, applications, storage, desktop, etc. Examples were explained to provide a thorough idea about how they are used in actuality.

The importance of big data and the necessity to mine information from the huge streams of data is brought to light as the various ways of sorting storing and managing data is discussed. The working Hadoop a platform to save big data framework and store and utilize data is explained. He touched upon a vast range of topics from the founding story of Google, to Radio-frequency identification, Application Programming Interfaces to Crypto currencies.

The subsections of the large domain Artificial Intelligence like Natural language processing, Automated reasoning, Machine learning and neural computing were briefed about. The applications of Artificial intelligence were talked about and how they came to be were discussed.



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A question arose about the differences between a data scientist and a computer scientist, and the difference between computer engineering and computer science. The doubts of the confused students were solved with a story explaining the task of each aspect of the course in a captivating way. He shared information with the students about how they can increase their knowledge, broaden their horizon and grow in the right direction. By referring to certain books, magazines and teaching the students that learning in engineering is a never-ending process to keep up with the advancing technology and fast paced competitive world.



### 4.5 Industrial Visit to GMRT

IETE-SF organized an industrial visit to Giant Metre wave Radio Telescope (GMRT), Pune, which was held on 9th March, 2018. The industrial visit was enlightening for all the students as the explanation was engaging and interactive. Students gained vast amounts of knowledge about radio telescopes as they got to see its real-life applications. Also, a lot of theoretical concepts and their importance was cleared as they got to see practical applications of those concepts. The students had sufficient time to ask doubts and their doubts were cleared with enthusiasm. The IETE-SF team was appreciated for their efforts for organizing and making this industrial visit a grand success.







#### 4.6 ICWiCOM 2017

Department of Electronics and Telecommunication Engineering organized the International Conference on Wireless Communication (ICWiCOM). The purpose of this international conference was to encourage the growth of research activities among Professors as well as students in various areas of the wireless communication engineering field. Over 100 research papers were submitted for the conference, out of which 35 selected papers were presented at the conference.

The conference was inaugurated at B J Hall with Dr. Surendra Pal, Vice Chancellor, DIAT Pune as the chief guest and Dr. Girish Kumar, Professor, IIT Bombay, as the guest of honour. Shri. Bharat M. Sanghvi, Vice President & Trustee, SVKM, Dr. Hari Vasudevan, Principal, Dr. A. C. Daptardar (Vice Principal, Admin), Dr. Manali J. Godse (Vice Principal, Acad) and Dr. Amit A. Deshmukh, Head of EXTC Department (D J Sanghvi College of Engineering) graced the inauguration with their presence.

There were two keynote addresses. The first keynote address was by the Chief Guest, Dr. Surendra Pal (Vice Chancellor, DIAT, Pune) and the second was by the guest of honour, Dr. Girish Kumar (IIT Bombay). It covered a wide range of topics in fields such as microwaves, antennas, wireless networking and signal processing for communication. Plenary talks by eminent speakers such as Dr. S.P. Duttagupta, Associate Professor at IIT Bombay, were also conducted on the research topics mentioned above. Engaging rounds of technical sessions were also conducted where research papers were presented by participants. The ensuing discussion and debates cultivated a rich environment of research-oriented thinking.



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#### 4.7 D J S ARYA (CANSAT)

CANSAT is a student satellite project, which involves the design and manufacturing of an canister satellite taking into consideration the mechanical, electronics and telecommunication parameters, in order to make a fully functional atmospheric satellite. The goal was to make a satellite which can replace weather balloons, simultaneously simulating the drop of a delicate instrument onto another planet's surface along with logging the weather data during descent.

DJS Arya had participated in the CANSAT competition that is organised by the American Astronomical Society and American Institute of Aeronautics and Astronautics. Here, they got an opportunity to design and build a student satellite under appropriate guidance. This competition bestows upon the students, a chance to represent their country and enhance various skills regarding the real life applications of a Canister Satellite. Moreover, it provides invaluable experience of working as a team at an international level. The mission guide for the same had been conducted on 20 October, 2017, followed by the Program Design Review on 25 January, 2018 and it had concluded with the Critical Design Report, which was conducted on 19 March, 2018.





## 4.8 D J Spark

The student chapter of IETE-SF organised 'DJ Spark 2018' a state level project-based technical paper presentation competition, on the 13th of April. It provided a platform for students to build their technical skills and showcase their talent in the form of projects. We received papers from around 90 groups consisting of over 250+ participants. After rigorous scrutiny by our Faculty Review Team, around 90 participants from various colleges were present on the day of the event showcasing 28 excellent papers and projects.

The event was inaugurated by the Head of Department of EXTTC, Dr. Amit A. Deshmukh, the IETE-SF branch counsellor, Prof. Anuja Odhekar, the Chairperson, Heetika Gada and the Vice-Chairperson, Aditti Agarwal. The Judges for the event were Mr Neeraj Gangrade and Mr Santosh Chapaneri.

The winning projects of the competition were-

1<sup>st</sup> Prize- Braille Display for the Visually Impaired

2<sup>nd</sup> Prize- Portable Healthcare Device

3<sup>rd</sup> Prize- Wireless Automatic Food Cooking for Basic Indian Meals

The winning team received a prize money of ₹12,000 whereas the first and second runner up teams were awarded a cash prize of ₹6,000 and ₹4,000 respectively. The papers of all the 28 projects have also been published in DJ Spark's very own technical journal.



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## 4.9 D J Strike

'D J Strike' is a mentorship program and an initiative which begun at the start of this academic year, also showcased their projects. Teams comprising of second year and third year students were given an opportunity to create industrial level projects under the guidance of BE mentors as well as a faculty mentor. The journey began with over 200 participants and 45 groups and 20 groups, 85 Students then got handpicked for the final demonstration and were evaluation. Their papers were published in our DJ Strike journal, which has a recognized ISBN number. The winning projects of the competition were-

1<sup>st</sup> Prize- RFID Based Money Transactions for Canteen Automation System

2<sup>nd</sup> Prize- Animatronic Hand Using Wireless Module

3<sup>rd</sup> Prize- Machine Learning based Heart disease Risk predictor

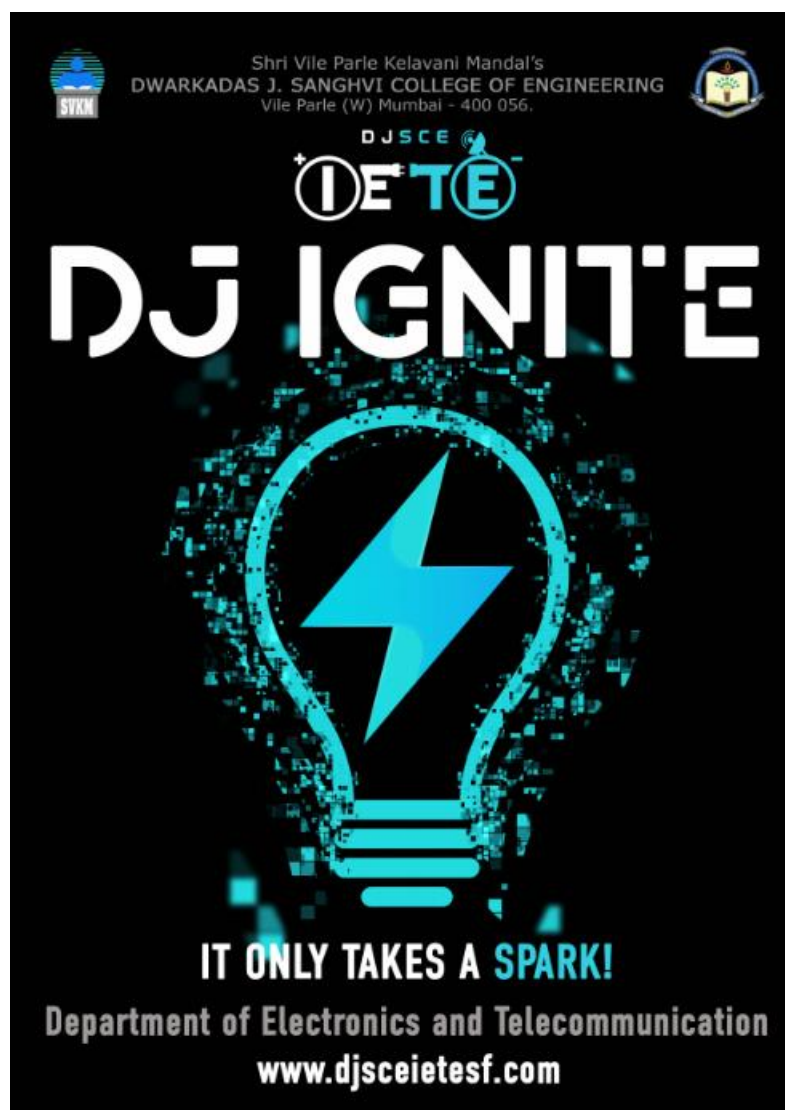
DJ Spark and DJ Strike has been a phenomenal success this year and we are confident that all the participants have upgraded their project building skills and technical writing skills throughout the course of the event.





#### 4.10 D J IGNITE

IGNITE is the official IETE-SF newsletter which contains articles as well as the reports of all the events conducted by IETE-SF throughout the year. Articles on Topics like big data analysis and Google's Keynote 2017 were covered in the 2018 feature. 'Into the future' section of IGNITE has articles dedicated to futuristic technology like augmented reality and automation. DJ IGNITE has attempted to provide solutions to the modern day problems by focusing on different technologies considering economic sustainability and demographical terms, hence inspiring the minds of our readers.





#### 4.11 Pre-placement Activity

As the placement season approaches, the final year students of Electronics and Telecommunication department organised a two-day workshop aiming to provide a brief idea on how the recruitment process is conducted. On the first day of the workshop (13th June 2018), an introductory session was held wherein the students were informed about the various stages of a general recruitment process, i.e., the aptitude test, the group discussion, and finally, the interviews. The type of questions included in an aptitude test, how to prepare for it, and what book should the students refer to were discussed. The students were also instructed on how a resume should be drafted, how to format the same and how to go about a complicated case study. In the latter part of the hour-long session, the aspects of the interview process (including guesstimates and puzzles) were deliberated.

Post the introductory session, a mock aptitude test was conducted. The aptitude test was set such that it included questions from logical reasoning, verbal ability, numerical ability, data interpretation, and core technical questions (electronics; IT). This helped the students understand the difficulty level and the form of questions a typical aptitude test consists of. The questions, as well as the solutions, were available to the students for later use.

On the second day of the workshop (14th June 2018), the students were divided into groups of 8/10 and group discussions were carried out. Each group was given a topic that they had to discuss for 10 minutes. A total of 4 group discussions were supervised and the students were given pointers on where they can improve their GD skills. Topics were chosen such that the students get a fair idea of the kind of topics encountered during any standard recruitment procedure. This session was followed by the interview rounds. The students were asked for their preference of non-technical and technical oriented interviews. In the tech-oriented interviews, the students were asked questions related to their projects, programming languages whereas in the non-tech interviews, the students had to solve puzzles, guesstimates and answer questions related to work ethics.

The workshop resulted in an overall success with students taking home key points on how to tackle tricky interview questions, the format their resume should be in, the kind of practice an aptitude exam requires and how to deal with intense group discussions.





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## 5. ACHIEVEMENTS

### 5.1 Faculty Publications- Conferences / Journals

Sr.No.	Name of Faculty	Publications
1	Dr. Amit Deshmukh	<p>[1] Amit A. Deshmukh, Aarti G. Ambekar, Venkata A. P. C., Akshay Doshi and K. P. Ray, "Modified U-slot Cut Rectangular Patch Antenna For Wideband Response", Proceedings of AEMC 2017, 19<sup>th</sup> - 22<sup>nd</sup> December 2017, Aurangabad, India (DOI: 10.1109/AEMC.2017.8325715)</p> <p>[2] Amit A. Deshmukh, Shefali Pawar, Aarti G. Ambekar, Prithish Kamble and K. P. Ray, "Compact Y-shape Antenna For Dual Polarized Wideband Response", Proceedings of AEMC 2017, 19<sup>th</sup> - 22<sup>nd</sup> December 2017, Aurangabad, India (DOI: 10.1109/AEMC.2017.8325714)</p> <p>[3] Amit A. Deshmukh, Megh Shukla, Stuti Patel, Saurabh Labde, and Venkata A. P. C., "Resonance Frequency Estimation For Equilateral Triangular Microstrip Antennas Using Artificial Neural Network Model", Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_8">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_8</a>)</p> <p>[4] Amit A. Deshmukh and Shefali Pawar, "Gap-coupled Designs of Compact F-shape Microstrip Antennas For Wider Bandwidth", Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_10">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_10</a>)</p>



		<p>[5] Amit A. Deshmukh, Anish Mishra, Forum Shah, Pooja Patil, Hetvi Shah and Arati G. Ambekar, "Variations of Slot cut Multi-band Isosceles Microstrip Antennas For Dual Polarized Response", Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_12">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_12</a>)</p> <p>[6] Amit A. Deshmukh, Poonam Kadam and Akshay Doshi, "Multi-Resonant Wide Band Rectangular Microstrip Antenna With U-shape and Rectangular Slots", Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_14">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_14</a>)</p> <p>[7] Amit A. Deshmukh, Venkata A. P. C. and Aarti G. Ambekar, "Analysis of Multi-Resonant Rectangular Microstrip Antenna Embedded With Multiple Slots", Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_16">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_16</a>)</p> <p>[8] Amit A. Deshmukh and Divya Singh, "Broadband Rectangular Microstrip Antennas Embedded With Pairs of Rectangular Slots", Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_17">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_17</a>)</p> <p>[9] Amit A. Deshmukh, Pritish Kamble, Akshay Doshi and Venkata A. P. C., "Multi-Resonator Variations of 120<sup>0</sup> Sectoral Microstrip Antennas For Wider</p>
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		<p>Bandwidth”, Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_19">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_19</a>)</p> <p>[10] Amit A. Deshmukh and Mohil Gala, “Partial Corner Edge Shorted Rectangular Microstrip Antenna Embedded With U-slot For Dual Band Response”, Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_20">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_20</a>)</p> <p>[11] Amit A. Deshmukh, Archana Nishad, Gauri Gosavi, Priyanka Narayanan, Siddharth Nayak and Aarti G. Ambekar, “Novel <math>\pi</math>-shape Microstrip Antenna Design For Multi-Band Response”, Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_21">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_21</a>)</p> <p>[12] Amit A. Deshmukh and Divya Singh, “On The Design of Wideband E-shape Microstrip Antennas On Varying Substrate Thickness”, Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_22">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_22</a>)</p> <p>[13] Amit A. Deshmukh, Anuja Odhekar, Akshay Doshi and Pritish Kamble, “Modified Circular Shape Microstrip Antenna for Circularly Polarized Response”, Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_23">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_23</a>)</p>
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		<p>[14] Amit A. Deshmukh and S. B. Deshmukh, "Wide Band Designs of 60<sup>0</sup> Sectoral Microstrip Antenna Using Parasitic Angular Sectoral Patches", Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_24">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_24</a>)</p> <p>[15] Jishnu P., Arnab Pattanayak, Siddhartha P. Duttagupta and Amit A. Deshmukh, "Elliptical Planar Dipole Antenna with Harmonic Rejection", Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_7">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_7</a>)</p> <p>[16] Akshita V. Nichani, Shruti T. Pistolwala, Amit A. Deshmukh and Manali J. Godse, "Telemedicine: Making Healthcare Accessible", Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_28">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_28</a>)</p> <p>[17] Ameya A. Kadam and Amit A. Deshmukh, "CPW-Fed Printed Monopole with Plus Shaped Fractal Slots for wider Bandwidth", Proceedings of ICWiCOM 2017, 19<sup>th</sup> &amp; 20<sup>th</sup> January 2018, Mumbai, India (<a href="https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_18">https://link.springer.com/chapter/10.1007%2F978-981-10-8339-6_18</a>)</p> <p>[18] Amit A. Deshmukh, Poonam A. Kadam, Prithish Kamble and Akshay Doshi, "Multiple Rectangular Slots Cut Rectangula Microstrip Antenna For Wide Band Response", Proceedings of ICCICT 2018, 2<sup>nd</sup> – 3<sup>rd</sup> February 2018, Mumbai, India (DOI:</p>
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		<p>10.1109/ICCICT.2018.8325896, Electronic ISBN: 978-1-5386-2051-9, CD-ROM ISBN: 978-1-5386-2050-2, Print on Demand(PoD) ISBN: 978-1-5386-2052-6)</p> <p>[19] Amit A. Deshmukh, Poonam A. Kadam, Akshay Doshi and Pritish Kamble, "Design and Analysis of Multiband Defected ground plane MSA" Accepted for publication in ICCICT 2018, 2<sup>nd</sup> – 3<sup>rd</sup> February 2018, Mumbai, India (DOI: 10.1109/ICCICT.2018.8325895)</p> <p>[20] Amit A. Deshmukh, Poonam A. Kadam, Akshay Doshi and Pritish Kamble, "Sectoral Patch Antenna Embedded with Arc Shape Slots and Slits For Circular Polarized Response" Accepted for publication in ICCICT 2018, 2<sup>nd</sup> – 3<sup>rd</sup> February 2018, Mumbai, India (DOI: 10.1109/ICCICT.2018.8325897)</p> <p>[21] Amit A. Deshmukh, Shafin Nagarbowdi and K. P. Ray, "Broadband Variations of Isosceles Triangular Microstrip Antennas (ITMSAs)", IEEE Antennas and Propagation Magazine, Vol. 60, no. 2, April 2018, pp. 34 – 47 (DOI: 10.1109/MAP.2018.2796019).</p> <p>[22] A. Pattanayak, G. Rana, S. P. Duttagupta, P. S. Gandhi, Amit A. Deshmukh, "Design of a 5 GHz Reflectarray with Reduced Size Unit Cell and Extremely Low Phase Sensitivity", Accepted for Publication in RF Radioengineering Journal.</p>
2	Prof. Ameya Kadam	<p>[1] Kadam A.A., Deshmukh A.A. (2018) CPW-Fed Printed Monopole with Plus Shaped Fractal Slots for Wider Bandwidth, Proceedings of International Conference on Wireless Communication, Lecture Notes on Data Engineering and Communications Technologies, vol 19. Springer, Singapore.</p>



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3	Prof. Shivani Bhattacharjee	<p>[1] Niharika Mehta, Shikhar Varma and Shivani Bhattacharjee, "Automatic Garbage Collector Bot Using Arduino and GPS" ICWiCOM 2017 during 19-20<sup>th</sup> January 2018.</p> <p>[2] Anamika Sen, Harsh Shah, Jessie Lemose and Shavani Bhattacharjee, "An Algorithm to Extract Handwriting Feature of Personality Analysis" ICWiCOM 2017 during 19-20<sup>th</sup> January 2018.</p>
4	Dr. Sunil Karamchandani	<p>[3] Megh Doshi, Maitri Fafadia, Charmi Gandhi, Sunil Karamchandani, "Cellulose Acitate Substrates for design and calibration of Strain Gauges in Angle measurement " ICWiCOM 2017 during 19-20<sup>th</sup> January 2018.</p> <p>[4] Aniruddha Garge, Sunil Karamchandani and Sweta Suhasaria, "Segmentation Technique for Differential Variations in Fingerprint Images", ICWiCOM 2017 during 19-20<sup>th</sup> January 2018.</p>
5	Prof. Ranjushree Pal	<p>[1] Ranjushree Pal, "Effect of Windowing in the performance of OFDM systems", ICWiCOM 2017 during 19-20<sup>th</sup> January 2018.</p>
6	Prof. Mrunalini Pimpale	<p>[1] "A Novel Approach Towards Handwritten Character Recognition Using Deep - Learning ", Mrunalini Pimpale, ICICCT 2018 during 20-21 April 2018.</p>
7	Archana Chaudhari	<p>[1] Aniket Kalkar, Abhiroop Mattiyil, Krupa Modi, Sagar Moharir, Archana Chaudhari, "Energy Efficient Solar Powered Weather Station and Soil Analyzer", ICWiCOM2017 during 19-20<sup>th</sup> January 2018.</p> <p>[2] Neeraj Ramkumar, Archana Chaudhari, "Urban Bus Arrival Time Prediction using Linear Regression and</p>





		Kalman Filter- A comparison”, ICSCSP-2018 during 22-23 <sup>rd</sup> June, 2018 at MRCET, Hyderabad.
8	Prof. Arati Ambekar	<p>[1] Amit A. Deshmukh, Venkata A.P.C.and Aarti G. Ambekar, ,,“Analysis of Multi-Resonant Rectangular Microstrip Antenna Embedded With Multiple Slots”, Proceedings of International Conference on Wireless Communication 2017, Mumbai , Springer, Singapore ,pp 139-149, <b>Print ISBN 978-981-10-8338-9</b></p> <p>[2] A.A. Deshmukh, Anish Mishra, Foram Shah, Pooja Patil, Hetvi Shah and Aarti G. Ambekar “Variations of Slot cut Multi-band Isosceles Microstrip Antennas For Dual Polarized response “,Proceedings of International Conference on Wireless Communication 2017, Mumbai , Springer, Singapore.pp. 103-110, <b>Print ISBN 978-981-10-8338-9</b></p> <p>[3] A A Deshmukh, Archana Nishad, Gauri Gosavi, Priyanka Narayanan, Siddharth Nayak and Aarti G. Ambekar ,” Novel pi–shape Microstrip Antenna Design For Multi-Band Response “,Proceedings of International Conference on Wireless Communication 2017, Mumbai , Springer, Singapore.pp. 185-193, <b>Print ISBN 978-981-10-8338-9</b></p>
9	Venkat Ramanan V.	[1] Aditi Patel, Abhishek Satpute, Mital Pattani and V. Venkat Ramanan, “Virtual Piano“ , ICWiCOM 2017 during 19-20 <sup>th</sup> January 2018.



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		[5] Jeet D. Sanghvi, Alay M. Shah, Saurabh S. Rane and V. Venkatramanan, "Smart Traffic Density Management System using Image Processing" ICWiCOM 2017 during 19-20 <sup>th</sup> January 2018.
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Sr. No.	Name of Faculty	Achievement
1.	Prof. Vishakha Kelkar	Certificate of appreciation for her instrumental role as SPOC for the NPTEL Local Chapter rated "AA"



## 5.2 Interaction of faculties with outside world

Sr. No.	Name of Faculty	Description
1.	Dr. Amit A. Deshmukh	<ol style="list-style-type: none"><li>1. Editor, Proceedings of ICWiCOM 2017, Springer Publication, (<a href="https://www.springer.com/in/book/9789811083389">https://www.springer.com/in/book/9789811083389</a>)</li><li>2. “Antenna Engineering and Applications”, Invited Lecture at SBMP, Mumbai, 27<sup>th</sup> March 2018</li><li>3. Examiner for PhD Annual Progress for Research Scholar at VJTI Mumbai</li><li>4. “Technical Paper writing – Authors and reviewer perspective” Lecture at STTP on Research Methodology at SBMP Mumbai, April 2018.</li><li>5. Served as a Reviewer for IEEE Access, IEEE Transactions on Antennas and Propagation, AEU - International Journal of Electronics and Communications, IETE Journal of Research</li><li>6. On Mumbai university staff selection panel for faculty selection at APSIT, ViMEET during June 2018</li><li>7. Lecture on “Analysis and Design of Wide band, Dual band and Compact Microstrip Antennas” at STTP in RF Antenna Design at KJSCE, Vidyavihar, Mumbai</li><li>8. Lecture on “Design of Microstrip Antennas – Resonant mode perspective” at STTP in Commercial Trends for Antennas and Microwave Technologies – inclination towards start-ups” at SFIT, Borivali, Mumbai</li></ol>
2.	Prof. Tanaji Biradar	<ol style="list-style-type: none"><li>1. Attended orientation program on EDC-II organized by SFIT on behalf of university of Mumbai.</li></ol>
3.	Prof. Vishakha Kelkar	<ol style="list-style-type: none"><li>1. Participated in one day workshop on “Enhancing Quality of Technical Education:P Issues and</li></ol>



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		Challenges” jointly organized by DTE, AICTE, WRO in association with Thakur college of engineering and technology, Mumbai on 21 <sup>st</sup> February 2018.
4.	Ranjushree Pal	1. Guest lecture conducted on Introduction and architecture of DSP processors on 21/4/2018 at Fr.C R C E, Bandra.
5.	Dr. Sunil H. Karamchandani	1. Participate in FDP on “R and Python Programming” conducted by department of Computer Engineering of D. J. Sanghvi College of Engineering during 12-13 <sup>th</sup> January 2018
6.	Prof. Archana Chaudhari	1. Attended orientation program on Linear Integrated Circuits organized by SFIT on behalf of university of Mumbai.
7.	Prof. Yukti Bandi	1. Attended orientation program on Signals and Systems organized by SFIT on behalf of university of Mumbai.



### 5.3 Students Achievements

Sr. No.	Name of Student	Event
1.	Siddharth Thakker (SE)	The DJSCE football team participated in Blaze '18 organised by the NMIMS school of commerce. The team attained the runners-up position in this tournament where many universities from Mumbai had participated.
2.	Siddharth Thakker (SE)	200m running, silver on DJSCE Sports day
3.	Anish Shetty (SE)	Shotput, Bronze on DJSCE Sports day
4.	Anagha Narayanan (TE)	100m running, silver on DJSCE Sports day
5.	Anagha Narayanan(TE)	200m running, silver on DJSCE Sports day
6.	Siddharth Thakker (SE) , Anagha Narayanan (TE) , Aishwarya Nair (TE), Lohit Shetty (FE))	Relay 4*100m Silver on DJSCE Sports day
7.	Prateek Chaturvedi	Presented paper on "A review: Methods to Lower Specific Absorption Rate for Mobile Phones", ICACCT organized by Amrutvahini College of Engineering, Sangmner during 8 <sup>th</sup> - 9 <sup>th</sup> Feb2018



## 6. Result Analysis

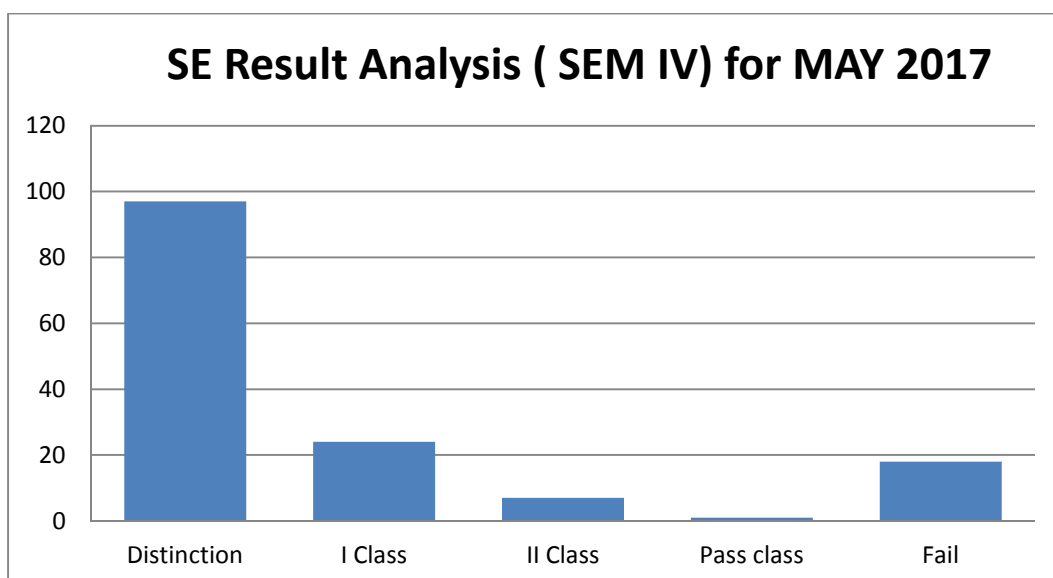
**Class:- SE**

No. of students: 148

	Distinction	I Class	II Class	Pass class	Fail
SE Result Analysis(SEM IV) DEC 2017	97	24	7	1	18

### Subject wise Result Analysis

Subject	Appeared	Pass
Applied Mathematics Iv	148	142
Electronic Devices And Circuitsi	148	134
Digital System Design	148	148
Circuit Theory And Network	148	142
Electronic Instruments And Control	148	147





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Name	Rank	GPA
Chitrey Ajinkeya	First	10
Pandey Anuradha	First	10
Kamath Deepti	First	10
Khetan Ashutosh	First	10
Magar Nikita	First	10
Mahabal Hrishikesh	First	10
Rane Sharwari	First	10
Shaikh Muddassir	First	10
Shinde Abhishek	First	10
Varma Advait	Second	9.92
Mehta Yatrik	Third	9.88



**Class: TE**

Total No. of students = 148

	Distinction	First class	Second class	Pass class	Fail
TE Result Analysis(SEM VI)- DEC 2017	27	64	27	0	26

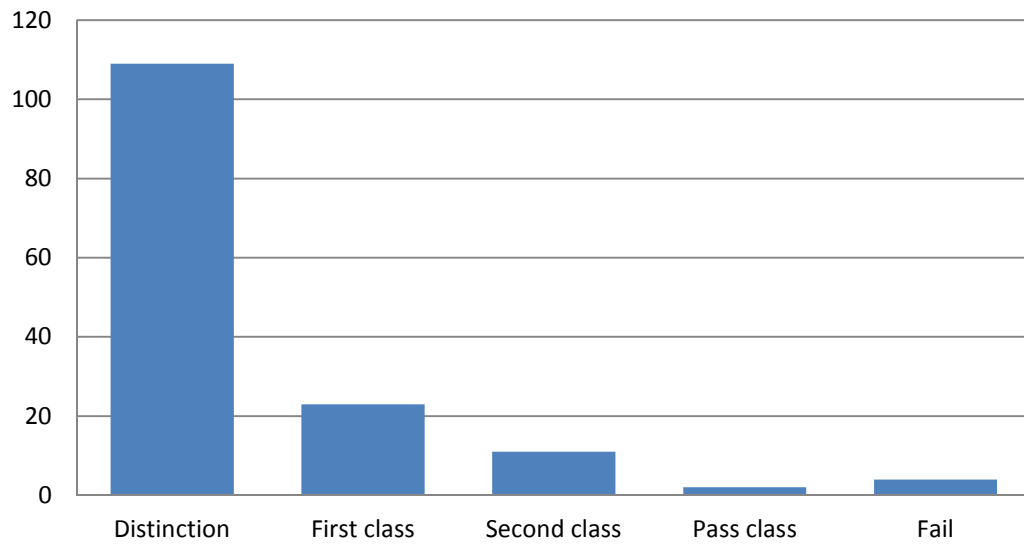
Subjectwise Result analysis

Subject	Appeared	pass
Microprocessor And Applications	147	145
Analog Communication	147	146
Random Signal Analysis	148	146
Rf Modelling And Antenna	148	147
Integrated Circuits	148	147
Microprocessor And Applications	147	145





### TE Result Analysis ( SEM V) for Dec 2016



Name	Rank	GPA
Baleri Panchami	First	10
Sankkhe Pratik	First	10
Shah Dharati	Second	9.96
Suhrid Subramanyam	Third	9.93
Ahluvalia Ayushman	Third	9.93
Bandeali Lizna	Third	9.93



**Class: BE**

Total No. of students = 137

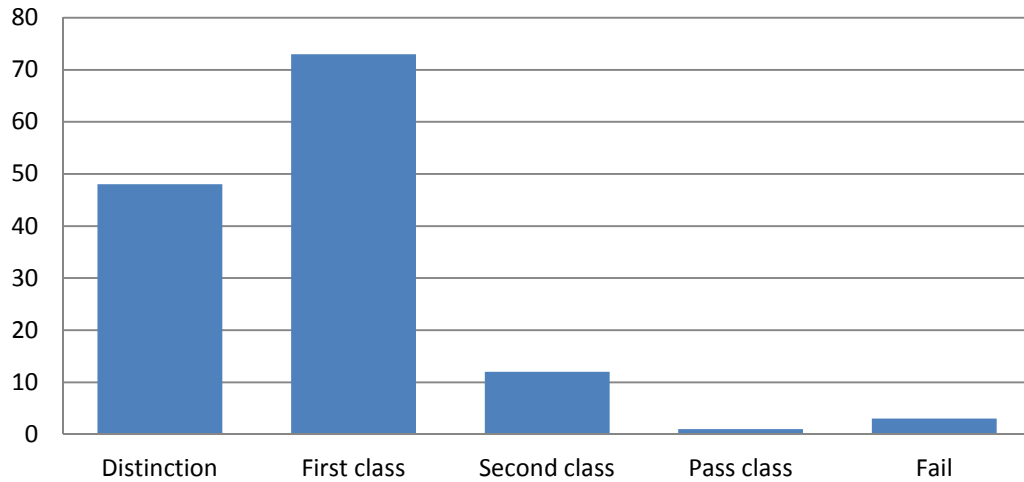
	Distinction	First class	Second class	Pass class	Fail
BE Result Analysis(SEM VIII)-DEC 2017	48	73	12	1	3

Subjectwise Result Analysis

Subject	Appeared	pass	fail
Image And Video Processing	137	133	4
Mobile Communication	137	137	
Optical Communication And Networks	137	136	1
Microwave And Radar Engineering	137	136	1
Elective 1 Nnfl	119	118	1
DCE	18	18	



## BE Result Analysis ( SEM VII) for MAY 2017



Name	Rank	GPA
ANKIT CHAWDA	First	9.52
YASH PARMAR	Second	9.26
VAIBHAVI KENI	Second	9.26
PRACHI GANDHI	THIRD	9.11
ZALA HARSHITA	THIRD	9.11



## 7. Placement Data

Sr. No.	Company Name	No. of Students Placed	Salary Per Annum(LPA)
1.	Pharmeasy (SD)	1	10
2.	Indus Valley Partners	1	7.48
3.	ZS Associates	3	6.75
4.	Halftick	1	6
5.	Oracle(OFSS)(D)	2	6
6.	Quantiphi	2	5.5
7.	NSE TECH	1	5.4
8.	Tresvista	3	5.08
9.	Usigma (D)	4	5
10	Time	1	4.8
11	ENY	3	4.5
12.	Cleartax	2	4.5
13.	LNTI	12	4.1
14.	Deloitte	3	4.1
15.	Selec	3	4
16.	Tata Communication	2	4
17.	Byjus	1	4
18.	Logiciqds	1	4
19.	Avalon Global	1	3.6
20	TCS	24	3.3
21.	Infosys	18	3.25
22	ATOS	5	3.1
23	Media.net	1	3.1
24	Relaince Retail	3	3.1
25	NSE IT	3	3
26	ICICI	4	2.4
27	Aufklaren	1	2.1
<b>Total Number of Students Placed (Companywise)</b>		<b>106</b>	
<b>Minimum CTC in LPA</b>		<b>2.1 LPA</b>	
<b>Maximum CTC in LPA</b>		<b>10.0 LPA</b>	